



BRIGHT LED ELECTRONICS CORP.

PHOTO LINK RECEIVER SPECIFICATION

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 REVISION: 1.0

● DEVICE NUMBER : BFRX-1101/H5

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佰鴻工業股份有限公司

BRIGHT LED ELECTRONICS CORP.

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APPROVED	DRAWN

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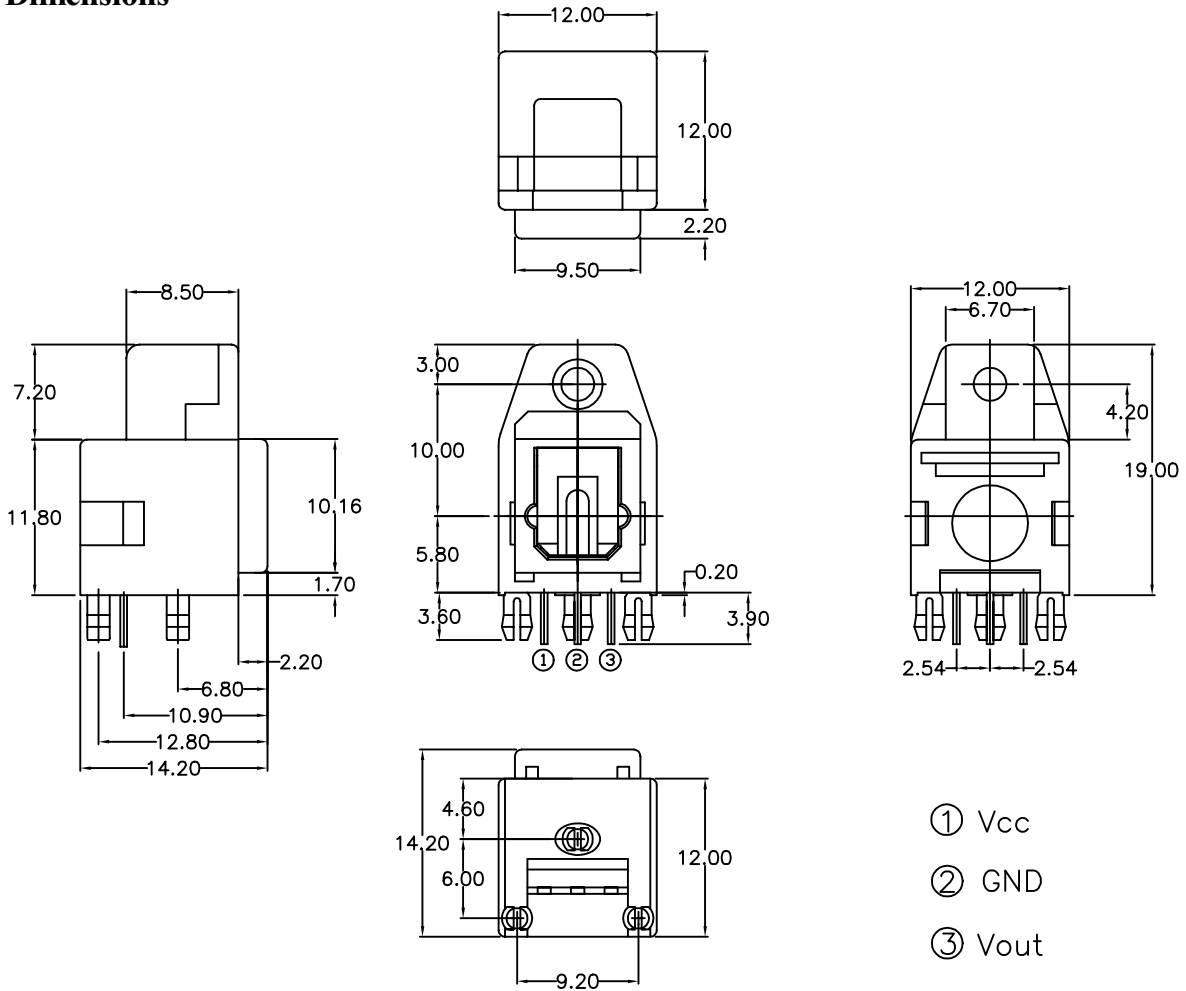
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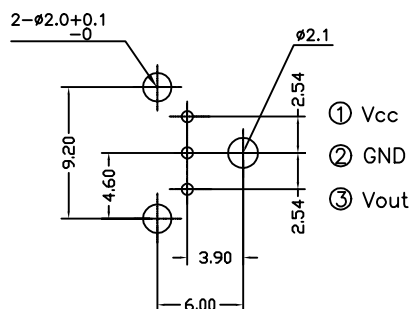
●Features:

1. Conform to EIAJ Standard CP-1201 (For Digital Audio Interface including Fiber Optic inter-connections).
2. ATC (Automatic Threshold Control) Circuit is used for stabilized output at a wide range of optical power level.
3. A self-tapping hole for easy attachment to the panel of Audio Equipments.
4. Operating voltage: 4.75 to 5.25 V.
5. Compatible sharp opto link with shutter type.

●Outline Dimensions



●Recommended drilling as viewed from the soldering face



NOTES: Tolerance is ± 0.3 mm unless otherwise noted.

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●Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Rating	Unit
Storage Temperature	Tstg	-40 to + 70	°C
Operating Temperature	Topr	-20 to + 70	°C
Supply Voltage	Vcc	-0.5 to + 7	V
Low level Output Current	I _{OL}	5	mA
High level Output Current	I _{OH}	-1	mA
Soldering Temperature	Tsol	260 ⁽¹⁾	°C

Note ⁽¹⁾ Soldering time ≤ 5 seconds (More than 1mm apart from package).

●Electro-Optical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Peak wavelength	λ_p		---	660	---	nm
Operating supply voltage	Vcc		4.75	5.0	5.25	V
Data rate	T	NRZ code	0.1	---	6.0	Mbps
Transmission Distance	D	Using APF (All Plastic Fiber, 970/1000 μ m) and BFTX-1001	0.2	---	5	m
Maximum Receivable Power	P _{MAX}	6Mbps, Using APF	-14.5	---	---	dBm
Minimum Receivable Power	P _{MIN}	6Mbps, Using APF	---	---	-24	dBm
Dissipation current	I _{cc}	Refer to Fig. 1	---	22	40	mA
High level output voltage	V _{oH}	Refer to Fig. 2	3.6	4.0	---	V
Low level output voltage	V _{oL}	Refer to Fig. 2	---	0.2	0.4	
Rise time	t _r	Refer to Fig. 2	---	20	40	ns
Fall time	t _f	Refer to Fig. 2	---	20	40	ns
Low→High delay time	t _{PLH}	Refer to Fig. 2	---	---	180	ns
High→Low delay time	t _{PHL}	Refer to Fig. 2	---	---	180	
Pulse width distortion	Δ tw	Refer to Fig. 2	-30	---	+30	
Jitter	Δ tj	Refer to Fig. 2	---	---	30	

●Mechanical Characteristics (Ta=25°C)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Insertion Force		* 1	---	---	40	N
Withdrawal Force		* 1	6	---	40	
Torque for Self-Tap		Using self-tapping screw (M3 x 8)	60	---	100	N-cm

* 1 : Using standard optical fiber cable (970/1000 μ m)

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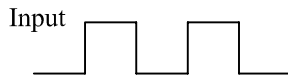
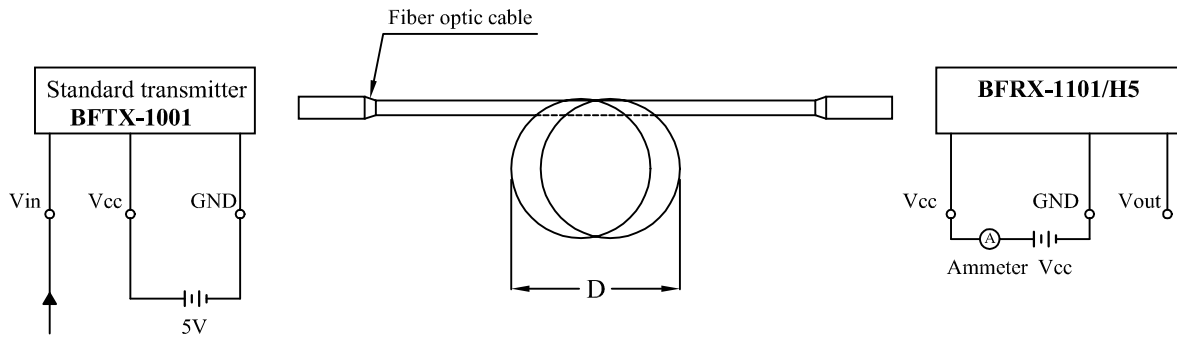
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●Fig.1 Measuring Method of Dissipation Current.



6 Mbps NRZ, Duty 50%

Notes: (1) $V_{cc}=5.0V$ (State of operating)

(2) To bundle up the standard fiber optic cable, make it into a loop with the diameter $D=10cm$ or more.

(3) $P_c = -14.5 \text{ dBm}$

(4) Measured on an ammeter

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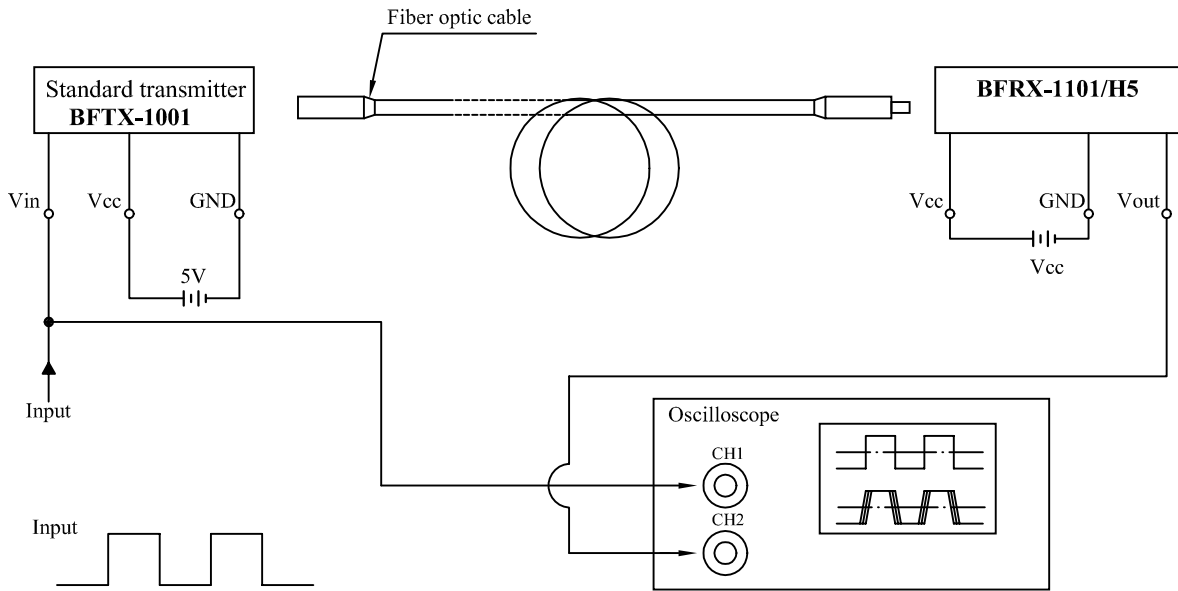
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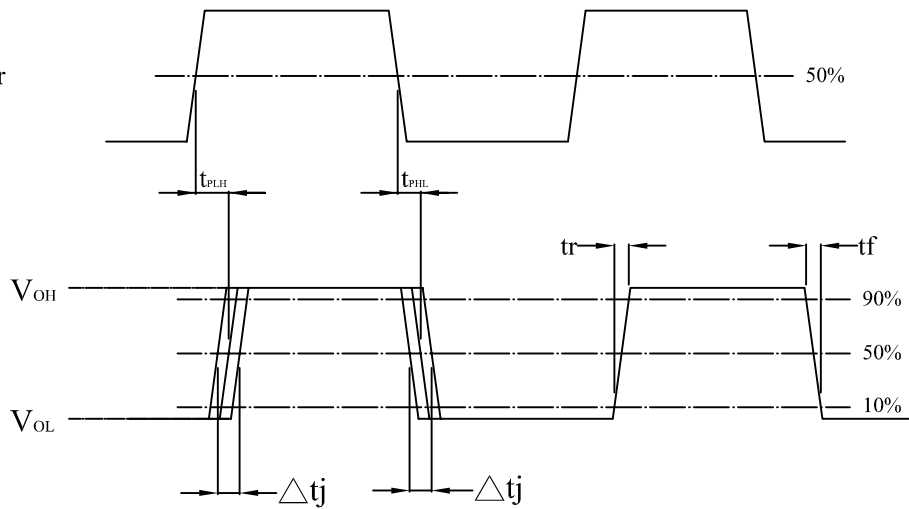
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● Fig.2 Measuring Method of Output Voltage and Pulse Response.



6 Mbps NRZ, Duty 50%

Standard transmitter
Input signal
(CH1)



Output SIGNAL
(CH2)

Test item

Test item	Symbol	Test item
Low→High pulse delay time	t_{PLH}	Refer to the above prescriptions.
High→Low pulse delay time	t_{PHL}	Refer to the above prescriptions.
Rise time	t_r	Refer to the above prescriptions.
Fall time	t_f	Refer to the above prescriptions.
High level output voltage	V_{OH}	Refer to the above prescriptions.
Low level output voltage	V_{OL}	Refer to the above prescriptions.
Pulse width distortion	Δtw	$\Delta tw = t_{PHL} - t_{PLH}$
Jitter	Δtj	Set the trigger on the rise of input signal to measure the jitter of the rise of output.

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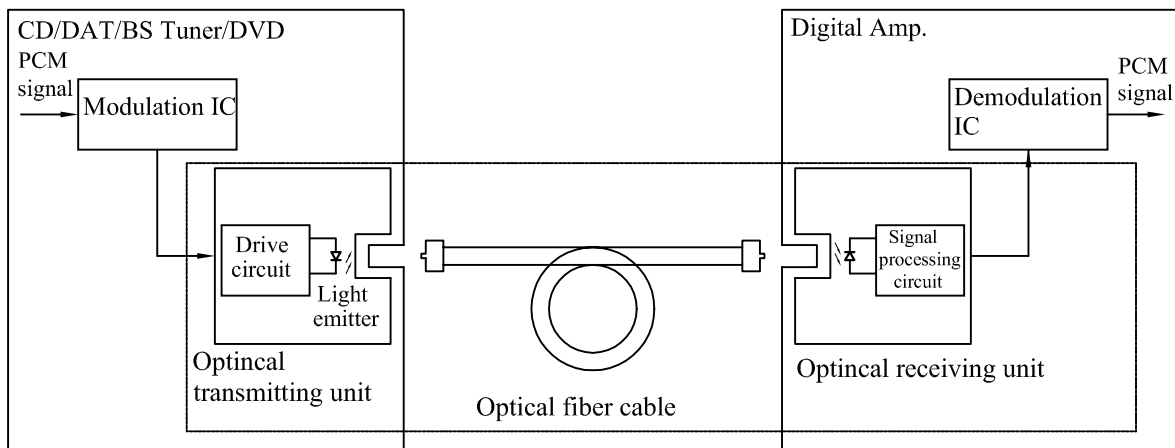
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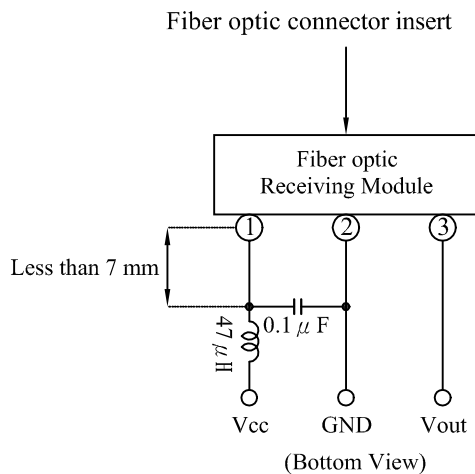
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● System Configuration Example:



● Recommended Connection Method:



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RELIABILITY TEST

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Classification	Test Item	Reference Standard	Test Conditions	Result
Endurance Test	Operation Life	MIL-STD-750:1026 MIL-STD-883:1005 JIS C 7021 :B-1	Connect with a power $V_{cc}=5V$ T_a =Under room temperature Test time=1,000hrs	0/20
	High Temperature High Humidity Storage	MIL-STD-202:103B JIS C 7021 :B-11	$T_a=85^{\circ}C \pm 5^{\circ}C$ RH=90%-95% Test time=240hrs	0/20
	High Temperature Storage	MIL-STD-883:1008 JIS C 7021 :B-10	High $T_a=105^{\circ}C \pm 5^{\circ}C$ Test time=1,000hrs	0/20
	Low Temperature Storage	JIS-C-7021 :B-12	Low $T_a=-55^{\circ}C \pm 5^{\circ}C$ Test time=1,000hrs	0/20
Environmental Test	Temperature Cycling	MIL-STD-202:107D MIL-STD-750:1051 MIL-STD-883:1010 JIS C 7021 :A-4	$-55^{\circ}C \sim 25^{\circ}C \sim 105^{\circ}C \sim 25^{\circ}C$ 30min 5min 30min 5min Test Time=10cycle	0/20
	Thermal Shock	MIL-STD-202:107D MIL-STD-750:1051 MIL-STD-883:1011	$-55^{\circ}C \pm 5^{\circ}C \sim 105^{\circ}C \pm 5^{\circ}C$ 10min 10min Test Time=10cycle	0/20
	Solder Resistance	MIL-STD-202:201A MIL-STD-750:2031 JIS C 7021 :A-1	$T_{sol}=260 \pm 5^{\circ}C$ Dwell Time= 5 ± 1 sec.	0/20
	Solder ability	MIL-STD-202:208D MIL-STD-750:2026 MIL-STD-883:2003 JIS C 7021 :A-2	$T_{sol}=230 \pm 5^{\circ}C$ Dwell Time= 5 ± 1 sec.	0/20
	Lead Bending Stress	MIL-STD-750:2036 JIS C 7021 :A-11	$0^{\circ} \sim 90^{\circ} \sim 0^{\circ}$ bend , 3 cycles Weight 250g	0/20

JUDGMENT CRITERIA OF FAILURE FOR THE RELIABILITY

Parameter	Symbol	Measuring conditions	Judgement criteria for failure
Receivable power	P_c	$V_{cc}=5V$	-14.5dBm~24dBm
Dissipation current	I_{cc}	$V_{cc}=5V$	Over $U \times 2$

Note: 1.U means the upper limit of specified characteristics.

2.Measurment shall be taken between 2 hours and after the test pieces have been returned to normal ambient conditions after completion of each test.

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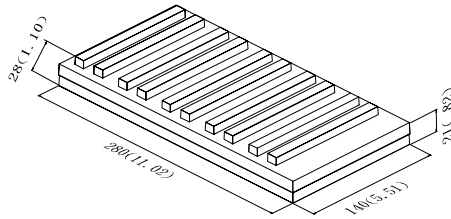
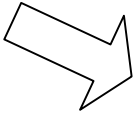
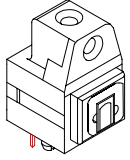
PACKAGING DIMMENSIONS

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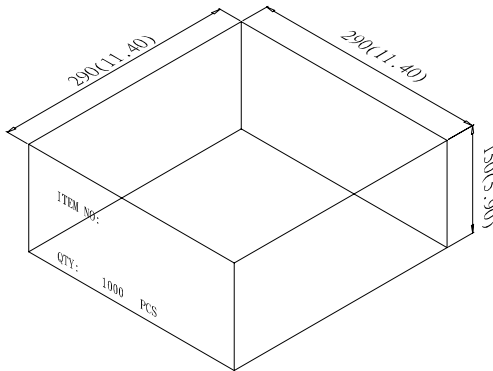
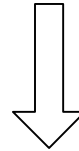
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● Package Method:(unit:mm)



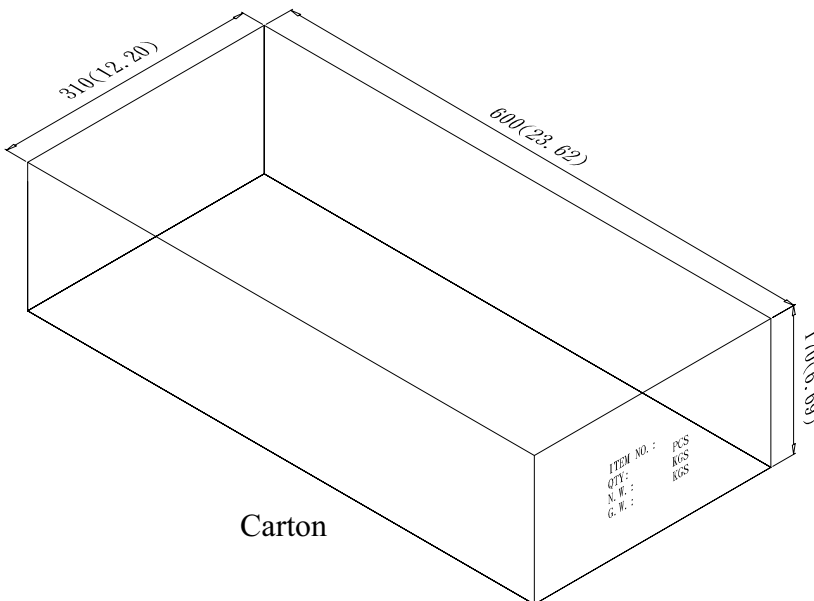
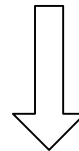
100 Pcs / Tray

Tray



10 Tray / Inner box

Inner box



2 Inner box / Carton

Carton

NOTES : Tray : Tolerance is ± 5 mm unless otherwise noted.

Inner box : Tolerance is ± 10 mm unless otherwise noted.

Carton : Tolerance is ± 10 mm unless otherwise noted.